

# Press Release

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## FOR IMMEDIATE RELEASE

## NeuroMatrix<sup>®</sup> Core is Available at Silicon Intellectual Property Market

**MOSCOW, Russia September 14, 1999** - Research Center MODULE, the owner of NeuroMatrix<sup>®</sup> DSP architecture today announced that NeuroMatrix Core (NMC) is available for licensing. NMC is a synthesizable Verilog RTL model of a high performance DSP core with VLIW/SIMD architecture. The core includes two main units: 32-bit RISC processor and 64-bit VECTOR co-processor to support vector operations with elements of variable bit length (Pat. 2131145 RU). The main features are:

- Silicon proven core (NM6403 DSP);
- Scalable performance (from 1 up to 288 multiplication/accumulations per cycle);
- Original instruction set;
- Small number of eq. gates (~80.000);
- Clock frequency - 123 MHz (8ns instruction cycle time) at 0,25 um CMOS technology;
- Strong Software Development Kit
- PCI Evaluation Board.

RC MODULE believes that new DSP technology will be used for integration with Application Specific Integrated Circuit (ASIC) libraries to provide high-performance integrated circuits for image processing, signal processing, neural net emulation, telecommunications, 3G wireless applications and crypto-processing.

The business and technical information will be available at DSP WORLD ICSPAT, November 4, 1999 in Orlando, Florida, USA and Brussels - EUREKA 48-th World Fair of Innovation, Research and new Technologies on November 10, 1999.

Information about NMC can be found on the World Wide Web at <http://www.module.ru>

### About RC MODULE

Research Center MODULE designs high-end processors architecture, embedded computers and application software for DSP applications and artificial neural networks. RC MODULE also provides system and ASIC design services to a variety of telecommunication and computer system OEMs.



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